

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Budong You et al.
Serial No. : 10/714,271
Filed : November 13, 2003

Art Unit : 2812
Examiner : Ron Everett Pompey
Confirmation No.: 2014
Notice of Allowance Date: October 5, 2006

Title : A METHOD OF FABRICATING A LATERAL DOUBLE-DIFFUSED MOSFET (LDMOS) TRANSISTOR AND A CONVENTIONAL CMOS TRANSISTOR

MAIL STOP ISSUE FEE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO NOTICE OF ALLOWANCE

In response to the Notice of Allowance mailed October 5, 2006, enclosed are a completed issue fee transmittal form PTOL-85b. The total fees in the amount of \$1000 for the issue fee (\$700) and the publication fee (\$300) are being paid concurrently herewith on the Electronic Filing System (EFS) by way of Deposit Account authorization. Please apply any additional charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,

Date: 12/8/06

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